

Form PTO-1449 (modified)	ATTY. DOCKET NO. 2069-019	SERIAL NO. 08/235,827
LIST OF PATENTS AND PUBLICATIONS STATEMENT (Use several sheets if necessary)	APPLICANT Cirrus Logic, Inc.	GROUP
	FILING DATE 04/28/94.	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number	Date	Name	Class	Sub-Class	Filing Date if Appropriate
	A 07/890,525		Uhlin & Lin			05/28/1992
JB	B 4,630,039	12/16/86	Shimada			
JB	C 4,746,981	05/24/88	Nadan et al.			
JB	D 5,293,474	03/08/94	Patil et al.			
	E					
	F					
	G					
	H					
	I					
	J					
	K					

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub-Class	TRANSLATION	
	L					YES	NO
	M						
	N						
	O						

OTHER ART (Including Author, Title, Date, Pertinent, Pages, Etc.)

JB	P	Sutty & Blair, Advanced Programmer's Guide to the EGA/VGA (1988), pp. 19-23, 26-27, 44-51
JB	Q	Ferraro, Programmer's Guide to the EGA and VGA Cards(1993), pp. 278-284..
	R	
	S	

Examiner <i>Jeffrey A. Brier</i>	Date Considered 8-16-95
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*Indicates that a copy of this reference has previously been furnished

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609;

Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.

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